

LowPPhy module improvization using novel methodology for 5G Technology

M.R. USIKALU¹, E. N. C. OKAFOR², D. ALABI³, G. N. EZEH⁴

^{1,2,3,4}Electrical and Electronic Engineering Department, University of Ibadan Ibadan, Nigeria

Email: Mr.usikalu@ui.edu.ng¹, okafor.enc@ui.edu.ng², alabi.d@ui.edu.ng³, ezeh.gn@ui.edu.ng⁴

Received: 14.08.22, Revised: 20.09.22, Accepted: 18.10.22

ABSTRACT

In a communication system, OFDM is the best technique for high speed transfer amplification in a communication network. The principal downside in the OFDM system was PAPR (Peak Average Power Reduction Ratio). Since of this large PAPR the device output reduces as the OFDM signal moves through the amplifier. The large PAPR multiplexing device in the Orthogonal Frequency division increases the power output of the RF amplifier and decreases the Non linearity of the HPA characteristics. Block labeling, clipping and filtering, selective mapping (SLM), partial transmitting scheme (PTS), tone reservation, tone injection are built to minimize PAPR. But because of the disadvantages of those techniques we look forward to another technique as companding. By combining the exponential companding and μ -law companding we can reduce the Peak to average power ratio in orthogonal frequency division multiplexing signals. We may decrease PAPR posed in orthogonal frequency division multiplexing, systems by implementing CCDF function on the companding technique.

Keywords: OFDM, PAPR, amplifier, Exponential companding, μ law companding, CCDF.

I. Introduction

OFDM is the primary methodology used in electronic communication network for the transfer of digital data. It can format the data with the same Frequency with specific carrier frequencies. Internal carrier guard bands are also not needed for transmission of the OFDM signal, and the message is transmitted orthogonally, indicating that the subcarriers are

orthogonal to each other. The data is separated into parallel streams encrypted on to the sub carriers, and the low symbol rate modulates the subcarriers. OFDM Signal Amplification and Demodulation use Quick Fourier Transform (FFT). In OFDM the sub carriers are not intersected and the carriers are separated from each other.

A. Block diagram of OFDM

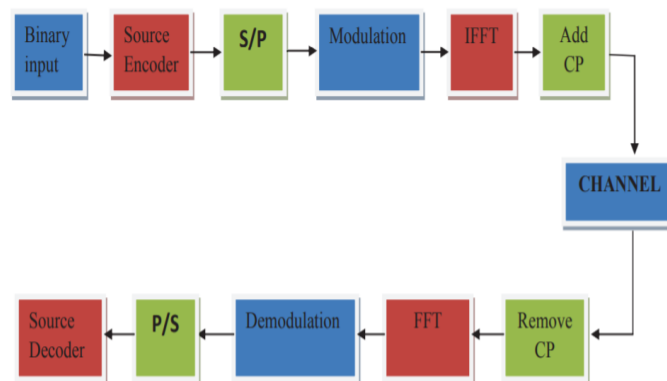


Fig. 1. Block diagram of OFDM transfer system

B. Working principle of OFDM

At transmitter we are giving input as 0's and 1's. So the input is given in the form of 0's and 1's. The serial input data source is converted to the appropriate word

size for transmitting and converted to parallel format. The data is then transmitted in parallel during the transmission, by applying every data word to one carrier.

We may use some modulation scheme at the modulation stage, such as BPSK, QPSK. Phases applied to the input signal. IFFT block transforms the domain frequency signal into a time domain signal. In the OFDM communication the cyclic prefix is used. The signal would be added to the Guard Section. So we'll get the amount of an orthogonal sinusoidal signal at the OFDM transmitter output.

At the receiver side decoder and demodulation is used to retrieve the original information. We have used FFT to convert time domain signal into the frequency domain signal. Remove cyclic prefix block is added to remove the guard band. Parallel to serial converter will convert the divided data into the serial and by using source decoder we will get the original information.

C. Wave form of OFDM

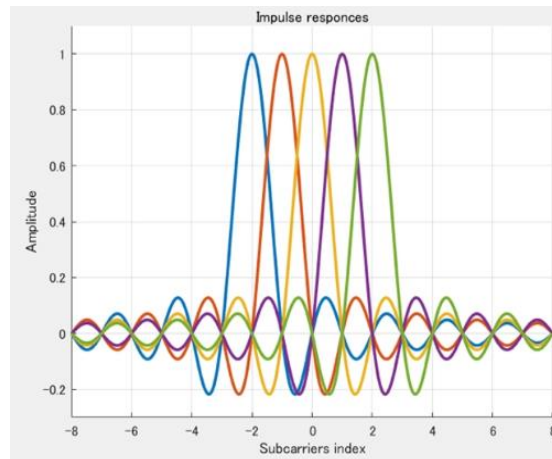


Fig. 2. wave form of OFDM system.

D. Advantages of OFDM

- Immunity to selective fading
- interference can be recovered quickly
- Spectrum efficiency
- Resilient to ISI

E. Disadvantages of OFDM

- High peak to average power ratio
- Sensitive to carrier offset and drift
- Requires multiple local oscillators

F. Applications of OFDM

- Digital audio broadcasting
- Asymmetric digital subscriber line
- Hiperlan2
- Wireless ATM-transmission system

G. What is PAPR

Peak to average reduction in power is described as the ratio of the maximum peak power to the average transmitted signal strength.

$$PAPR = \frac{P_{peak}}{P_{average}} = \frac{\max [x_n |^2]}{E [x_n |^2]} \quad (1.1)$$

Where x_n = An OFDM signal after IFFT

Owing to this large PAPR ratio the amplifier's output capacity is declining.

So, the main aim of this project is to reduce the high PAPR ratio in OFDM system. By reducing the amplitude of the signal or by decreasing the power we can reduce the high PAPR.

Because of this high PAPR the linearity of the output power of the amplifier becomes non linear. That is shown in below diagram.

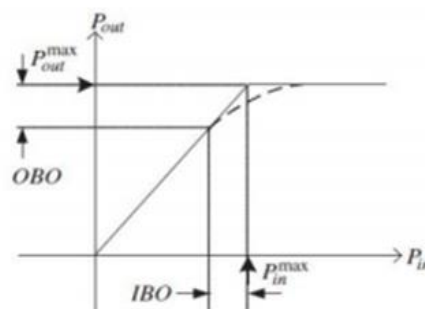


Fig. 3. characteristics of HPA

H. Cause for the occurrence of the PAPR

The PAPR happens because at any instance in time the subcarriers are out of step with each other and collinear to each other, they become distinct and separate. As all the sub carrier signals concurrently hit the maximum value, this will allow the output to unexpectedly go to the peak value at the output envelope.

Wide numbers of sub carriers are modulated; the peak value is thus very high relative to the device average value. We may name this ratio "max to average power reduction factor"

II. Literature Survey

H.P. Moose[1] mentioned regarding OFDM in 1994 is a multi-carrier transmission strategy for large communications systems of data rates. The OFDM concept relies on the delivery of high-speed data across a broad variety of low-speed carriers. The carriers are intimately related, and the Strong Fourier transformation (FFT) produces separation of frequencies between them. The OFDM originates from the Multiplexing Frequency Division (FDM), which communicates over various carrier frequencies with even more than one small-rate signals. Signal isolation at the receiver is accomplished in FDM by bringing the signals far sufficiently apart, such that the channel spectra will not interfere.

Dae woo Lim, Jong Seon No, seok joong Heo [2] in 2009 discussed methods for combating OFDM's key drawback, its extreme peak-to-average power ratio (PAPR), which induces signal distortion while going through High Power Amplifier and Digital-to-Analog Converter, resulting in lower average level of power. And for the latter question several other approaches have to be applied. Many researchers have suggested several approaches to address. The PAPR issue that involves technique of block coding, technique of filtering and clipping, technique of selective mapping , technique of partial transmission (PTS), tone reservation and tone injection.

H. Imai and H. Ochiai [3] in 1997 were explained about block coding technique. In this technique they have used QPSK modulation. The fundamental idea of this technique is to send the symbols which are having the low peak power for transmission. If there are N subcarriers and they are represented in 2N bits for the modulation. The transmitted bits are divided into half and allocated to the sub blocks, the remaining half bits again divided into half and this process is continued until N bits are allocated to the N sub carriers. This long series of information is partitioned and assigned to half sub blocks and that blocks are encoded. This coding also gives the error correction capability, for that they have used different coding techniques that are Hamming, Shannon fano, etc.

Seung Hee Han, Jae Hong Lee [4] in 2005 was explained Simple and easiest technique to reduce the PAPR is clipping, it won't allow the signal amplitude above the clip level. It exhibits the noise and that noise is called as clipping noise or clipping distortion. And also expands the signal spectrum it causes interfering and it causes degradation of the BER. Clipping and filtering is removes the expanded components in spectrum. Filtering requires reducing the frequency production. After the clipping operation Filtering operation will be performed.

Baumel, R., Fischer, R., and Huber, J. [5] in 1996 was written that in the selective mapping technique the data coming from the source is serial but, the serial to parallel converter will convert data from serial to parallel. For Example the original data is X after the S/P converter the data is dividing to sub vectors $[x_1, x_2, \dots, x_m]$ and this sub vectros are multiplied with the different phase values $[p_1, p_2, \dots, p_m]$ before the IFFT operation. Each vector should multply with the each phase value independently. This sub vectors and the phase values are having the same length. After this the IFFT is applied to the siganl that translates time domain data to frequency domain. The last step is to compare all the individual signals and the signal which are having the lowest PAPR that signal is send for the transmission.

Manoranjan RaiBharti, Kamal Singh, Sudhanshu Jamwa [6] in 2012 was explained about partial transmit technique (PTS). Partial transmit techniques is one of the most efficient technique for reduction of the PAPR. The data blocks are separated into sub-blocks in this methodology. Such sub-blocks must independently execute IFFT procedure to transform fourier transform to time domain. This converted time domain sub blocks are multiplied with the weighing factor for phase rotation individually. The last step is selecting the low PAPR value signals add them and send for the transmission.

B. Krongold, D. Jones [7] in 2004 proposed a technique to reduce PAPR that is Tone reservation. This technique is reserving the small tones and those tones are called peak reduction carriers for reducing the PAPR. Both tones are applied to each other's input signal, which are orthogonal. The amount of PAPR reduction based on the number of assigned tonalities and the position of the designated tones. By reserving small tones at the transmitter we can reduce the maximum PAPR. This method is less complex. After reserving the tones the carriers are set as peak channel for reduce the peaks. Before the IFFT peak channel carriers and data carriers are added. After the IFFT operation the carriers are send for the transmission.

Md. Zulfiker Mahmud, Md. Ibrahim Abdullah, Md. Shamim Hossain, Md. Nurul Islam [8] in 2011 was explained about one of the PAPR reduced technique

as Tone Injection. This form of tone injection is used to minimize PAPR by growing the scale of the constellations. This method contrasts the data points of the original constellation with the data points of the enlarged constellation to increase the size of the constellation. That means data points found in the details are mapped into the expanded constellation. Drawbacks to this strategy are the encoding to data points on the receiver side is harder and needs more IFFT operations.

Lekhraj Udaigiriya and Sudhir Kumar Sharma [9] in 2015 were proposed a technique as complementary cumulative distribution function (CCDF) curves for the reduction of the PAPR reduction. It focuses only on the power levels and power characteristics. It provides distortion less signal without degradation of BER. This is the application they were performed on the companding techniques to achieve better results on the PAPR. The proposed techniques are having the disadvantages because of that they were applied this CCDF function on companding techniques.

Yong-Hua, and Yang Yang, Tao Jiang [10] in 2005 were explained about companding technique as exponential companding. In this section, we suggest a new non-linear companding technique, dubbed "exponential companding," which can effectively reduce the PAPR of distributed Orthogonal Frequency Division Multiplexing signals by transforming the amplification statistics of such signals into linear distribution. The latest scheme also has the benefit of retaining an overall continuous power output.

X. Wang, T. T. Tjhung, and C. S. Ng [11] in 1999 proposed another technique as μ law technique to

reduce the PAPR. μ law companding has two operations one is compressor and another one is expander. Compressor is placed after the IFFT block in the transmitter and the expander is placed before the FFT block in the receiver. It compresses the signal according to the give value. If the value of μ is high then the compression level also high. And it is the memory less technique it will compress the signal based on current value not on the past values. The companding transformation is applied in the transmitter to amplify the high peaks and amplify the low amplitudes of an OFDM signal, thereby reducing the PAPR. The decompressing approach is applied at the receiver when extracting the OFDM signal from the extended signal using inverse companding function. Companding is an enticing method for the reduction of PAPR of OFDM signals due to its less complexity.

III. Methodology

A. Complementary Cumulative Distribution Function (CCDF)

In PAPR reduction CCDF is the main method we are using. It focuses only on the power levels and power characteristics. It provides distortion less signal without degradation of BER. This is the application we are performing on the companding techniques to decrease the PAPR. The proposed techniques are having the disadvantages because of that we are applying this CCDF curves on companding.

$$\text{Power} = I^2 + Q^2$$

Where I= In phase component of the signal

Q= Quadrature component of the signal

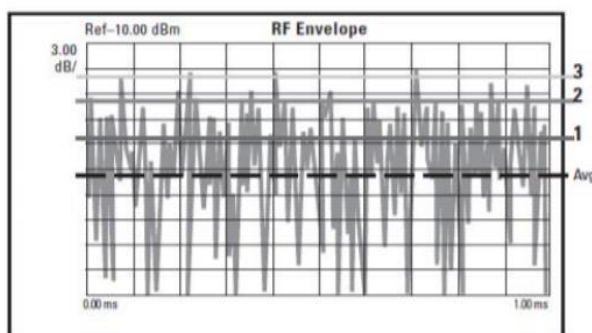


Fig. 4. Construction of CCDF curves

The above diagram shows the CCDF curves of the OFDM signal. The signal presented in the fig is difficult to quantify because of the distortion. So, we need to extract the required information signal. We can extract the signal from this distortion by applying

CCDF on it. It only spends the time at power level or above the power level with respect to the average value. Each line showing in fig is power level above the average value.

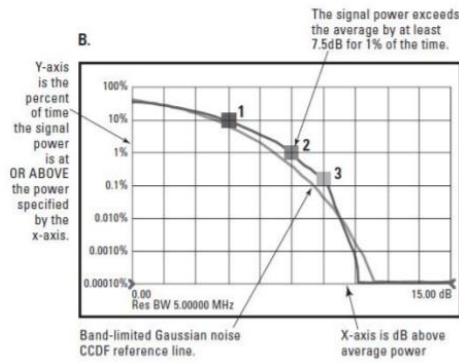


Fig. 5. CCDF curves on OFDM signal

Fig 3.2 shows that the OFDM signal after applying of CCDF curves. X axis is power in dB and Y axis is percent of time signal power is above or at average power. For example at 1% of time the peak power exceeds the average power by 7.5dB. So in this project we have used that CCDF curves to decrease the high PAPR present in orthogonal frequency division multiplexing system at the transmitter.

Those proposed techniques are more efficient are attractive to decrease the PAPR but, the problem in that is if we apply those technique individually we are facing some issues about distortion or BER performance. We can apply CCDF for those techniques individually to get the better results than the previous.

The main title of this project is A HYBRID PAPR REDUCTION TECHNIQUE IN OFDM SYSTEM that means combing of two or more techniques. Our idea is to combine companding techniques and apply a CCDF curves on it. We have done that application by using simulation Tool MATLAB software.

B. Proposed PAPR reduction techniques

The suggested strategies to developing the high PAPR in OFDM system are companding techniques. The companding transformation is applied in the transmitter to amplify the high peaks and amplify the low amplitudes of a OFDM signal, thereby reducing the PAPR. The decompanding approach is applied at the receiver when extracting the OFDM signal from the extended signal using inverse companding function. Companding is an enticing method for rising OFDM PAPR signals due to its less complexity. Companding strategies come in two forms.

- 1) μ law companding technique
- 2) Exponential companding technique

C. μ -law companding

μ law companding has two operations one is compressor and another one is expander. Compressor is placed after the IFFT block in the transmitter and the expander is placed before the FFT block in the receiver. This compander will compress the large signal and the expander will expands the small signal.



Fig. 6. Block diagram of μ law companding

The above diagram is the block diagram of μ law companding technique. At the transmitter first the data is enters through the modulator and then the data will converted into parallel streams through serial

to parallel convertor. If any large signal is there the compressor will compress that signal. Next block is Digital to Analog convertor will convert the digital data to the analog data.

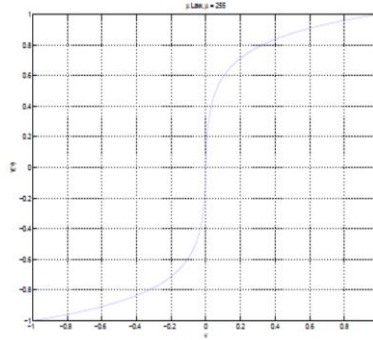


Fig. 7. μ law companding graph

This is the example for the instantaneous companding. It compresses the signal according to the value given. If the value of μ is high then the compression level also high. The Fig 4.1 is the graph with the μ value is 255. And it is the memory less technique it will compress the signal based on current value not on the past values.

$$\gamma(v) = \frac{\log_2(1+\mu v)}{\log_2(1+\mu)}, v > 0 \quad (4.1)$$

$$\gamma(v) = -\gamma(-v), v < 0 \quad (4.2)$$

Based on the above formula the signal will be compressed.

The PAPR formula for companding technique

$$PAPR_{comp} = \frac{\max_{0 \leq n \leq N-1} N \ln(1+\mu[P_{peak}]^{0.5})}{\sum_{n=0}^{N-1} \ln(1+\mu[P_n(t)]^{0.5})} \quad (4.3)$$

The CDF is given by

CDF=

$$\left[\frac{\ln(1+\mu)}{\mu \sigma^2} \left(\left(1 - e^{-\frac{z^2}{2\sigma^2}} \right) + \mu \left(\operatorname{erf} \left(\frac{z}{\sqrt{2\sigma}} \right) - \sqrt{\frac{2}{\pi}} z e^{-\frac{z^2}{2\sigma^2}} \right) \right) \right]^N \quad (4.4)$$

CCDF= 1-CDF

$$(4.5)$$

D. Exponential companding

In this segment we introduce a new nonlinear companding method, called "exponential companding," which can efficiently minimize the PAPR of transmitted (companded) OFDM signals by converting the statistics of such signals' amplitudes towards a standardized distribution. The latest scheme also has the benefit of carrying the nonlinear companding process at a steady average power point.

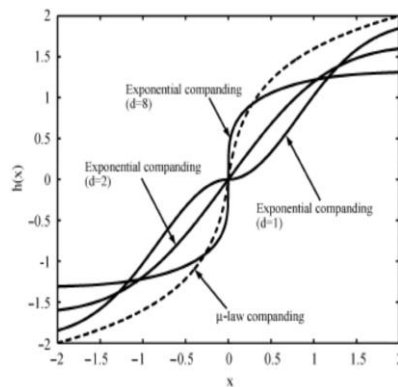


Fig. 8. Exponential companding graph

The companded signals transmitted amplitudes and competencies equally, for the situations $d=1$ and $d=2$. If, the suggested feature will concurrently compact broad input signals and extend tiny signals. Whereas the companding system will only magnify minor signals and does not alter the signal peaks, resulting in higher overall performance signal strength values.

The discrepancies between exponential companding functions are, as shown, ignorant of when $d \geq 8$.

The CDF of the companded signal is

$$F_{t_n}(x) = \operatorname{Prob}\{t_n \leq x\} = \operatorname{Prob}\{t_n^d \leq x^d\} \quad (4.6)$$

(4.7)

$$= x^d / \alpha \quad (4.8)$$

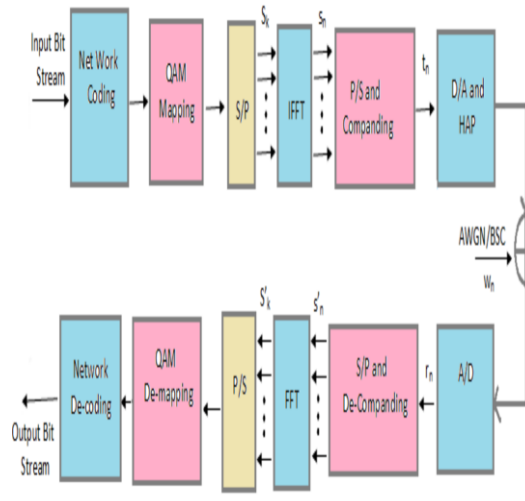


Fig. 9. Block diagram of Exponential companding

E. Performance analysis of Bit Error Rate on companding signals

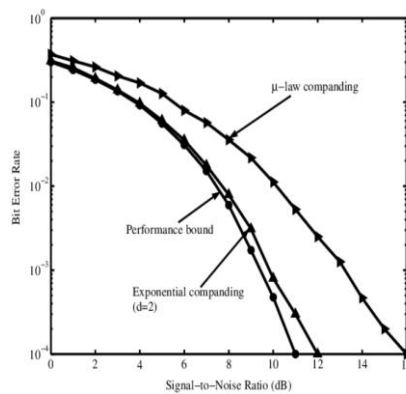


Fig. 10. Bit Error Rate of the companded signal in AWGN channel

Above diagram shows the relationship between the BER (bit error rate) and SNR (noise signal ratio). Companding gives the high SNR and increases the performance of the BER by compress or expands the OFDM signal.

IV. Results and Discussions

A. Partial Transmit Scheme (PTS)

The first curve showing with the blue color is PAPR of the original OFDM signal. By applying partial transmit scheme (PTS) small amount of PAPR is reduced that is shown in red color curve.

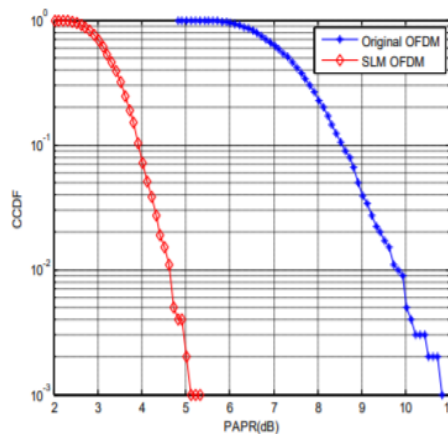


Fig. 11. Reduced PAPR curve after applying PTS technique

Advantages of PTS

- Less distortion
- Less complex

Disadvantages of PTS

- Degradation of BER performance
- Side information is required

B. Selective Mapping Technique (SLM)

The curve showing with the blue color is PAPR of original OFDM signal. The curve showing with the red color is reduced PAPR by applying selective mapping technique (SLM). It reduced the large amount of PAPR better than the Partial technique scheme (PTS).

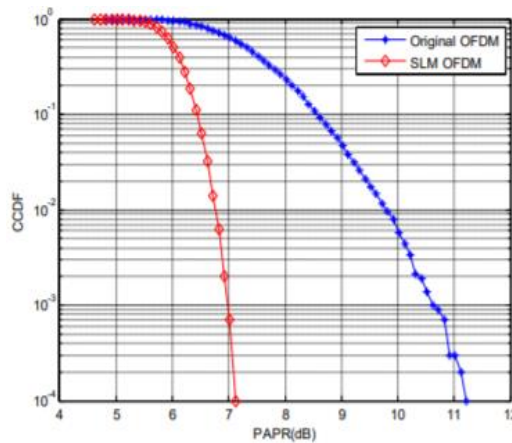


Fig 5.2: Reduced PAPR curve after applying SLM technique

Advantages

- No distortion
- It is not depending on number of carriers

Disadvantages

- Degradation of BER performance
- Side information is required.

C. Clipping and Filtering

PAPR is reduced by using clipping and filtering it is easier and best technique to reduce the PAPR in OFDM. The curve showing with the blue color is PAPR of original OFDM signal. The curve showing with the red color is reduced PAPR by applying clipping and filtering. Because of non linear process of clipping it produce out of band interference (OBI).

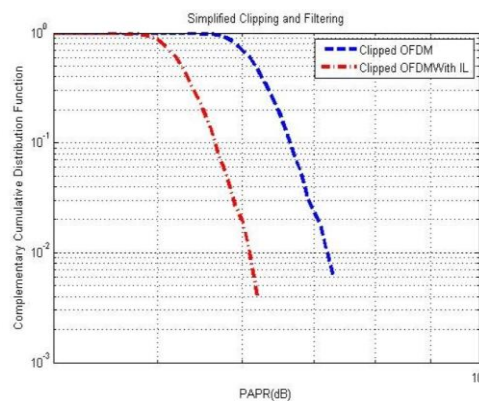


Fig 5.3 Reduced PAPR curve after applying clipping and filtering

Advantages

- Simple approach
- No side information is required

Disadvantages

- More distortion
- Degradation of BER performance

D. Comparison of proposed reduction techniques of PAPR

The below diagram shown that the comparison between the reduced techniques of PAPR. PAPR is reduced with the distorted signal. Blue color curve is the PAPR of original OFDM signal. Red color curve is the PAPR signal after applying the Partial Transmit Technique (PTS). Black color curve is the PAPR signal after applying the Selective Mapping Technique (SLM).

Pink color curve is the PAPR signal after applying the clipping and filtering technique.

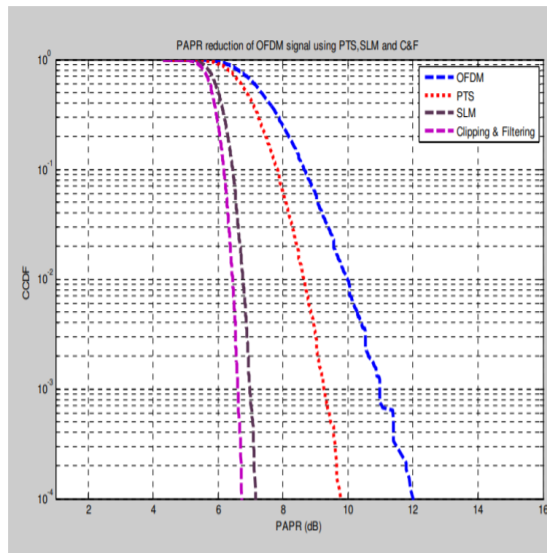


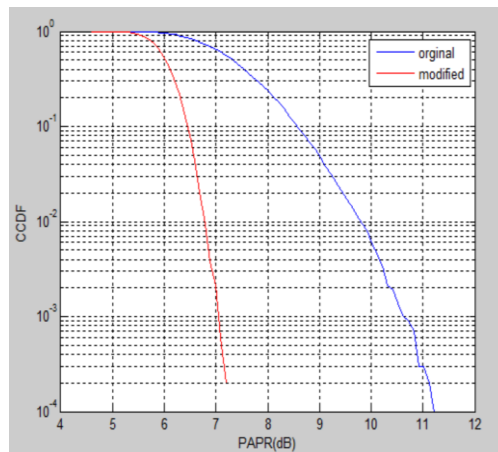
Fig 5.4: comparison of different PAPR reduced techniques

E. PAPR is reduced by using companding techniques

The below diagram is the simulated output of the CCDF curve to decrease the PAPR. The color curve in blue reveals the strong PAPR of the initial OFDM signal. The red color curve shows the decreased PAPR

after applying the CCDF curves on the companding techniques.

By adding the exponential technique and μ -law technique we have reduced the PAPR by using the CCDF function. This produces stronger outcomes than the methods suggested, such as Clipping, SLM, PTS.



F. Reduced PAPR of OFDM signal using companding techniques

We have simulated the above graph by using MATLAB R2013a software. Advantage of this companding technique is it improves the better performance of the BER and decreases the OBI. Disadvantage of companding technique is loss of data rate.

V. Conclusion

We've identified the OFDM definition and the OFDM issues. We summarize the definition for PAPR which is the key problem in OFDM but they have reduced the PAPR using developed techniques. But we have to

look with another CCDF definition because of the disadvantages of such strategies. By integrating all the strategies and carrying out CCDF activity on them, we simulated and obtained the results for the signal amplification with the less peak-to-average power reduction ratio. This work has two directions PAPR reduction technique should define best solution in terms of output enhancement and extend for less transmission distortion. Data speeds are small and channel use is effective. Future scope of this project can be in different ways. Based on the idea of this project it was found that the above all proposed techniques are not fully specified for reduction of

PAPR because of the disadvantages. The companding techniques also have the disadvantage of loss of data rate. So before taking the techniques to reduce the PAPR we have to take the factors like BER performance, data rate, distortion, complexity into consideration.

References

1. Mohammad khadir, S. Shakthi, S. Lakshmanachari, Vallabhuni Vijay, S. China Venkateswarlu, P. Saritha, Rajeev Ratna Vallabhuni, "QCA Based Optimized Arithmetic Models," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-5.
2. P. Ashok Babu, P. Sridhar, and Rajeev Ratna Vallabhuni, "Fake Currency Recognition System Using Edge Detection," 2022 Interdisciplinary Research in Technology and Management (IRTM), Kolkata, India, February 24-26, 2022, pp. 1-5.
3. Koteswaramma, K. C., Vallabhuni Vijay, V. Bindusree, Sri Indrani Kotamraju, Yasala Spandhana, B. Vasu D. Reddy, Ashala S. Charan, Chandra S. Pittala, and Rajeev R. Vallabhuni, "ASIC Implementation of An Effective Reversible R2B Fft for 5G Technology Using Reversible Logic," Journal of VLSI circuits and systems, vol. 4, no. 2, 2022, pp. 5-13.
4. Vijay, Vallabhuni, Kancharapu Chaitanya, Chandra Shaker Pittala, S. Susri Susmitha, J. Tanusha, S. China Venkateswarlu, and Rajeev Ratna Vallabhuni, "Physically Unclonable Functions Using Two-Level Finite State Machine," Journal of VLSI circuits and systems, vol. 4, no. 01, 2022, pp. 33-41.
5. Vijay, Vallabhuni, M. Sreevani, E. Mani Rekha, K. Moses, Chandra S. Pittala, KA Sadulla Shaik, C. Koteswaramma, R. Jashwanth Sai, and Rajeev R. Vallabhuni, "A Review On N-Bit Ripple-Carry Adder, Carry-Select Adder And Carry-Skip Adder," Journal of VLSI circuits and systems, vol. 4, no. 01, 2022, pp. 27-32.
6. Vijay, Vallabhuni, Chandra S. Pittala, A. Usha Rani, Sadulla Shaik, M. V. Saranya, B. Vinod Kumar, RES Praveen Kumar, and Rajeev R. Vallabhuni, "Implementation of Fundamental Modules Using Quantum Dot Cellular Automata," Journal of VLSI circuits and systems, vol. 4, no. 01, 2022, pp. 12-19.
7. Gollamandala Udaykiran Bhargava, Vasujadevi Midasala, and Vallabhuni Rajeev Ratna, "FPGA implementation of hybrid recursive reversible box filter-based fast adaptive bilateral filter for image denoising," Microprocessors and Microsystems, vol. 90, 2022, 104520.
8. Chandra Shaker Pittala, Rajeev Ratna Vallabhuni, Vallabhuni Vijay, Usha Rani Anam, Kancharapu Chaitanya, "Numerical analysis of various plasmonic MIM/MDM slot waveguide structures," International Journal of System Assurance Engineering and Management, 2022.
9. Chandra Shaker Pittala, Vallabhuni Vijay, B. Naresh Kumar Reddy, "1-Bit FinFET Carry Cells for Low Voltage High-Speed Digital Signal Processing Applications," Silicon, 2022. <https://doi.org/10.1007/s12633-022-02016-8>.
10. M. Saritha, M. Lavanya, G. Ajitha, Mulinti Narendra Reddy, P. Annapurna, M. Sreevani, S. Swathi, S. Sushma, Vallabhuni Vijay, "A VLSI design of clock gated technique based ADC lock-in amplifier," International Journal of System Assurance Engineering and Management, 2022, pp. 1-8. <https://doi.org/10.1007/s13198-022-01747-6>
11. B. M. S. Rani, Vallabhuni Rajeev Ratna, V. Prasanna Srinivasan, S. Thenmalar, and R. Kanimozhi, "Disease prediction based retinal segmentation using bi-directional ConvLSTM-Net," Journal of Ambient Intelligence and Humanized Computing, 2021, pp. 1-10. <https://doi.org/10.1007/s12652-021-03017-y>
12. Vallabhuni Vijay, C. V. Sai Kumar Reddy, Chandra Shaker Pittala, Rajeev Ratna Vallabhuni, M. Saritha, M. Lavanya, S. China Venkateswarlu and M. Sreevani, "ECG Performance Validation Using Operational Transconductance Amplifier with Bias Current," International Journal of System Assurance Engineering and Management, vol. 12, iss. 6, 2021, pp. 1173-1179.
13. Vallabhuni Vijay, Pittala Chandra shekar, Shaik Sadulla, Putta Manoja, Rallabhandy Abhinaya, Merugu rachana, and Nakka nikhil, "Design and performance evaluation of energy efficient 8-bit ALU at ultra low supply voltages using FinFET with 20nm Technology," VLSI Architecture for Signal, Speech, and Image Processing, edited by Durgesh Nandan, Basant Kumar Mohanty, Sanjeev Kumar, Rajeev Kumar Arya, CRC press, 2021.
14. Kiran, K. Uday, Gowtham Mamidiseti, Chandra shaker Pittala, V. Vijay, and Rajeev Ratna Vallabhuni, "A PCCN-Based Centered Deep Learning Process for Segmentation of Spine and Heart: Image Deep Learning," In Handbook of Research on Technologies and Systems for E-Collaboration During Global Crises, pp. 15-26. IGI Global, 2022.
15. Vallabhuni Vijay, V.R. Seshagiri Rao, Kancharapu Chaitanya, S. China Venkateswarlu, Chandra Shaker Pittala, Rajeev Ratna Vallabhuni, "High-Performance IIR Filter Implementation Using FPGA," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-5.
16. Jujavarapu Sravana, S.K. Hima Bindhu, K. Sharvani, P. Sai Preethi, Saptarshi Sanyal,

- Vallabhuni Vijay, Rajeev Ratna Vallabhuni, "Implementation of Spurious Power Suppression based Radix-4 Booth Multiplier using Parallel Prefix Adders," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-6.
17. Chandra Shaker Pittala, Vallabhuni Vijay, A. Usha Rani, R. Kameshwari, A. Manjula, D.Haritha, Rajeev Ratna Vallabhuni, "Design Structures Using Cell Interaction Based XOR in Quantum Dot Cellular Automata," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-5.
 18. S. China Venkateswarlu, Mohammad khadir, V. Vijay, Chandra Shaker Pittala, Rajeev Ratna Vallabhuni, "Optimized Design of Power Efficient FIR Filter Using Modified Booth Multiplier," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-5.
 19. G. Naveen, V.R Seshagiri Rao, Nirmala. N, Pavan kalyan. L, Vallabhuni Vijay, S. China Venkateswarlu, Rajeev Ratna Vallabhuni, "Design of High-Performance Full Adder Using 20nm CNTFET Technology," 4th International Conference on Recent Trends in Computer Science and Technology (ICRTCST-2021), Jamshedpur, India, February 11-12, 2022, pp. 1-5.
 20. L. Babitha, U. Somanaidu, CH. Poojitha, K. Niharika, V. Mahesh, and Vallabhuni Vijay, "An Efficient Implementation of Programmable IIR Filter for FPGA," 1st International Conference on Innovations in Signal Processing and Embedded systems (ICISPES-2021), Hyderabad, India, October 22-23, 2021.
 21. K. C. Koteswaramma, Ande Shreya, N. Harsha Vardhan, Kantem Tarun, S. China Venkateswarlu, and Vallabhuni Vijay, "ASIC Implementation of division circuit using reversible logic gates applicable in ALUs," 1st International Conference on Innovations in Signal Processing and Embedded systems (ICISPES-2021), Hyderabad, India, October 22-23, 2021.
 22. Vallabhuni Vijay, J. Sravana, K.S. Indrani, G. Ajitha, A. Prashanth, K. Nagaraja, K.C. Koteswaramma, C. Radhika, M. Hima Bindu, N. Manjula, "A SYSTEM FOR CONTROLLING POSITIONING ACCORDING TO MOVEMENT OF TERMINAL IN WIRELESS COMMUNICATION BASED ON AI INTERFACE," The Patent Office Journal No. 50/2021, India. Application No. 202141055995 A.
 23. Dr. L.V. Narasimha Prasad, Dr. Vijay Vallabhuni, Dr. S. China Venkateswarlu, Dr. V. Vhandra Jagan Mohan, Ms. P. Sruthilaya, Mr. K. Tarun Kumar, Mr. B. Raju, Mr. P. Ravinder, "Garbage Collector with Smart Segregation and Method of Segregation Thereof," The Patent Office Journal No. 04/2022, India. Application No. 202141062270 A.
 24. Sravana, J., K. S. Indrani, M. Saranya, P. Sai Kiran, C. Reshma, and Vallabhuni Vijay, "Realisation of Performance Optimised 32-Bit Vedic Multiplier," Journal of VLSI circuits and systems, vol. 4, no. 2, 2022, pp. 14-21.
 25. V. Vijay, J. Prathiba, S. Niranjan Reddy, V. Raghavendra Rao, "Energy efficient CMOS Full-Adder Designed with TSMC 0.18 μ m Technology," International Conference on Technology and Management (ICTM-2011), Hyderabad, India, June 8-10, 2011, pp. 356-361.
 26. Ch. Srivalli, S. Niranjan reddy, V. Vijay, J. Pratibha, "Optimal design of VLSI implemented Viterbi decoding," National conference on Recent Advances in Communications & Energy Systems, (RACES-2011), Vadlamudi, India, December 5, 2011, pp. 67-71.
 27. S. Swathi, S. Sushma, C. Devi Supraja, V. Bindusree, L. Babitha and Vallabhuni Vijay, "A Hierarchical Image Matting Model for Blood Vessel Segmentation in Retinal Images," International journal of system assurance engineering and management, vol. 13, iss. 3, 2022, pp. 1093-1101.
 28. Bandi Mary Sowbhagya Rani, Vasumathi Devi Majety, Chandra Shaker Pittala, Vallabhuni Vijay, Kanumalli Satya Sandeep, Siripuri Kiran, "Road Identification Through Efficient Edge Segmentation Based on Morphological Operations," Traitement du Signal, vol. 38, no. 5, Oct. 2021, pp. 1503-1508.
 29. M. Lavanya, Malla Jyothsna Priya, Ponukumatla Janet, Kavuluri Pavan Kalyan, and Vijay Vallabhuni, "Advanced 18nm FinFET Node Based Energy Efficient and High-Speed Data Comparator using SR Latch," International Conference On Advances In Signal Processing And Communication Engineering (ICASPACE 2021), Hyderabad, India, July 29-31, 2021.
 30. J. Sravana, K.S. Indrani, Sankeerth Mahurkar, M. Pranathi, D. Rakesh Reddy, and Vijay Vallabhuni, "Optimised VLSI Design of Squaring Multiplier using Yavadunam Sutra through Deficiency Bits Reduction," International Conference On Advances In Signal Processing And Communication Engineering (ICASPACE 2021), Hyderabad, India, July 29-31, 2021.
 31. Ratna, Vallabhuni Rajeev, and Ramya Mariserla. "Design and Implementation of Low Power 32-bit Comparator." (2021).
 32. Vallabhuni Vijay, Kancharapu Chaitanya, T. Sai Jaideep, D. Radha Krishna Koushik, B. Sai Venumadhav, Rajeev Ratna Vallabhuni, "Design of Optimum Multiplexer In Quantum-Dot Cellular Automata," International Conference on

Innovative Computing, Intelligent Communication and Smart Electrical systems (ICSES -2021), Chennai, India, September 24-25, 2021.

33. S. Sushma, S. Swathi, V. Bindusree, Sri Indrani Kotamraju, A. Ashish Kumar, Vallabhuni Vijay, Rajeev Ratna Vallabhuni, "QCA Based Universal Shift Register using 2 to 1 Mux and D flip-flop," IEEE 2021 International Conference on Advances in Computing, Communication and Control (ICAC3'21) 7th Edition (3rd and 4th December 2021), Mumbai, Maharashtra, India, December 03-04, 2021, pp. 1-6.
34. M. Sreevani, S. Lakshmanachari, B. Manvitha, Y.J.N. Pravalika, T.Praveen,V.Vijay, Rajeev Ratna Vallabhuni, "Design of Carry Select Adder Using Logic Optimization Technique," IEEE 2021 International Conference on Advances in Computing, Communication and Control (ICAC3'21) 7th Edition (3rd and 4th December 2021), Mumbai, Maharashtra, India, December 03-04, 2021, pp. 1-6.
35. M. Saritha, Chelle Radhika, M. Narendra Reddy, M. lavanya, A. Karthik, Vallabhuni Vijay, Rajeev Ratna Vallabhuni, "Pipelined Distributive Arithmetic-based FIR Filter Using Carry Save and Ripple Carry Adder," Second IEEE International Conference on Communication, Computing and Industry 4.0 (C2I4-2021), Bengaluru, Karnataka, India, December 16-17, 2021, pp. 1-6.
36. S. Swathi, S. Sushma, V. Bindusree, L Babitha, Sukesh Goud. K, S. Chinavenkateswarlu, V. Vijay, Rajeev Ratna Vallabhuni, "Implementation of An Energy-Efficient Binary Square Rooter Using Reversible Logic By Applying The Non-Restoring Algorithm," Second IEEE International Conference on Communication, Computing and Industry 4.0 (C2I4-2021), Bengaluru, Karnataka, India, December 16-17, 2021, pp. 1-6.
37. Ch. Srivalli, S. Niranjan reddy, V. Vijay, J. Pratibha, "Low power based optimal design for FPGA implemented VMFU with equipped SPST technique," National Conference on Emerging Trends in Engineering Application (NCETEA-2011), India, June 18, 2011, pp. 224-227.
38. Vallabhuni Vijay, and Avireni Srinivasulu, "A Novel Square Wave Generator Using Second Generation Differential Current Conveyor," Arabian Journal for Science and Engineering, vol. 42, iss. 12, 2017, pp. 4983-4990.